



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Yukihiko AOKI et al.

Serial No. :

09/941,288

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August 28, 2001

For

NETWORK ERROR DISPLAY APPARATUS AND ERROR

DETECTION DISPLAY METHOD

Art Unit

2133

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SEP 1 7 2003

Technology Center 2100

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450
Alexandria, VA 22313-1450, on September 12, 2003

Glenn F. Savit, Reg. No. 37,437

Name of Applicant, Assignee or
Registered Representative

Signature

September 12, 2003 Date of Signature

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97(b)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. 1.56, applicants are filing this Information Disclosure Statement and the accompanying form PTO-1449 along with copies of the prior art therein identified. Such prior art has come to the attention of applicants by being cited in a Search Report issued

in the corresponding European application on July 18, 2003. So far as applicants are aware, this Information Disclosure

Statement is being filed before the mailing date of the first

Office Action on the merits.

The filing of this Information Disclosure Statement is not an admission that the documents identified herein constitute prior art to the present application.

Please charge any fees incurred by the filing of this document or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP Attorneys for Applicants

D17.

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